

Dialog DataStar

[options](#)[logout](#)[feedback](#)[help](#)[databases](#)[search
page](#)

Titles

To view one or many selected titles scroll down the list and click the corresponding boxes. Then click display at the bottom page. To view one particular document click the link above the title to display immediately.

[copy to
Clipboard](#)

Documents 1 to 20 of 20 from your search "**((najm-f\$) AND power AND estimation) AND VLSI**" in all the available information:

Number of titles selected from other pages: 0

☐ **Select All**

☐ 1 [display full document](#)

2007. (INZZ) Maximum circuit activity **estimation** using pseudo-Boolean satisfiability.

☐ 2 [display full document](#)

2005. (INZZ) Early **power estimation** for **VLSI** circuits.

☐ 3 [display full document](#)

2004. (INZZ) **Power estimation** techniques for FPGAs.

☐ 4 [display full document](#)

2003. (INZZ) Energy and peak-current per-cycle **estimation** at RTL.

☐ 5 [display full document](#)

2001. (INZZ) **Power estimation** for large sequential circuits.

☐ 6 [display full document](#)

1998. (INZZ) Statistical **estimation** of the switching activity in **VLSI** circuits.

☐ 7 [display full document](#)

2000. (INZZ) Analytical models for RTL **power estimation** of combinational and sequential circuits.

☐ 8 [display full document](#)

2000. (INZZ) **Power** modeling for high-level **power estimation**.

☐ 9 [display full document](#)

1999. (INZZ) Energy-per-cycle **estimation** at RTL.

☐ 10 [display full document](#)

1999. (INZZ) High-level area and **power estimation** for **VLSI** circuits.

☐ 11 [display full document](#)

1998. (INZZ) **Low-power** design methodology: **power estimation** and optimization.

☐ 12 [display full document](#)

1997. (INZZ) High-level area and **power estimation** for **VLSI** circuits.

☐ 13 [display full document](#)

1995. (INZZ) **Power estimation** techniques for integrated circuits.

☐ 14 [display full document](#)

1995. (INZZ) Pattern independent maximum current **estimation** in **power** and ground buses of CMOS **VLSI** circuits: Algorithms, signal correlations, and their resolution.

☐ 15 [display full document](#)

1995. (INZZ) Feedback, correlation, and delay concerns in the **power estimation** of **VLSI** circuits.

☐ 16 [display full document](#)

1994. (INZZ) A survey of **power estimation** techniques in **VLSI** circuits.

☐ 17 [display full document](#)

1994. (INZZ) Estimating **power** dissipation in **VLSI** circuits.

☐ 18 [display full document](#)

1993. (INZZ) A Monte Carlo approach for **power estimation**.

☐ 19 [display full document](#)

1989. (INZZ) Electromigration median time-to-failure based on a stochastic current waveform.

☐ 20 [display full document](#)

1990. (INZZ) Probabilistic simulation for reliability analysis of CMOS **VLSI** circuits.

Selection	Display Format	Output Format	ERA SM Electronic Redistribution & Archiving
<input checked="" type="radio"/> from this page <input type="radio"/> from all pages	<input checked="" type="radio"/> Full <input type="radio"/> Free <input type="radio"/> Short <input type="radio"/> Medium <input type="radio"/> Custom Help with Formats	<input checked="" type="radio"/> HTML <input type="radio"/> Tagged (for tables) <input type="radio"/> PDF <input type="radio"/> RTF <input type="radio"/> XML	Copies you will redistribute: <input type="text"/> Employees who will access archived record (s): <input type="text"/> Help with ERA
<div> Sort your entire search result by <input type="text" value="Publication year"/> <input type="button" value="v"/> <input type="text" value="Ascending"/> </div>			

copy to
Clipboard

Top - News & FAQs - Dialog

© 2007 Dialog



Customize your homepage

[Web](#) | [Images](#) | [Wikipedia](#) | [Video](#) | [More »](#)

[Sign in](#) | [Preferences](#)

"semiconductor power" + "derivative".

Web Search

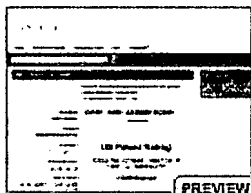
Adv

☐ All the web ☒ Pages written in English

Web

Results 1-3 of about 3 for "semiconductor power" + "derivative"

View:



Method for estimating the service life of a power semiconductor ...

5521524 Method and system for screening ... load changes, calculating a derivative dP/dN of the electrical parameter P according to the number ...

www.patentstorm.us/patents/5877419.html - 17k -
Add to shortcuts

Narrow your search

Your refinements

Languages

• English [remove](#)

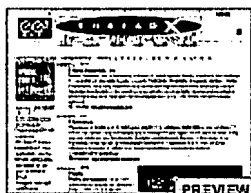
[More choices »](#)



Robust adaptive fuzzy control for permanent magnet synchronous ...

Takagi-Sugeno-type fuzzy logic systems are used to approximate uncertain functions. ... The closed-loop system is proven to be semiglobally uniformly ultimately ...

www.robo-erectus.org/members/czhou/Paper/YangZhouIJS05.pdf - 07 Sep 2006 - 217k -
Add to shortcuts



companies (p) - tutorials free design (p)

IEI Technology: PICMG, PC/104, Embedded, RISC, PCI Express & more ... interfacing solutions for the PCI Express, PCI, PCI-X, and derivative protocols.

www.eg3.com/WebID/411/p/blank/company/10-p-p.htm - 25k - Add to shortcuts

Results page: 1

"semiconductor power" + "derivative"

Web Search

☐ All the web ☒ Pages written in English



Dissatisfied with results? [Help us improve.](#)

[Our Blog](#) | [Create an account](#) | [Jobs Opportunities](#) | [Enterprise solutions](#) | [Site Map](#) | [Go to Exalead.com](#)

Find what you're looking for with Exalead, the innovative worldwide Web search engine.

Searching 8,042,419,228 web pages - © 2000-2007 exalead

DERWENT-ACC-NO: 1989-043574

DERWENT-WEEK: 198906

COPYRIGHT 2007 DERWENT INFORMATION LTD

TITLE: Power system mimic device for simulation - has computer
to calculate power system characteristic, and wave form
generator circuit NoAbstract Dwg 0/7

PATENT-ASSIGNEE: HITACHI LTD[HITA]

PRIORITY-DATA: 1987JP-0152485 (June 19, 1987)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 63316615 A	December 23, 1988	N/A	010	N/A

APPLICATION-DATA:

PUB-NO	APPL-DESCRIPTOR	APPL-NO	APPL-DATE
JP 63316615A	N/A	1987JP-0152485	June 19, 1987

INT-CL (IPC): H02J003/00

ABSTRACTED-PUB-NO:

EQUIVALENT-ABSTRACTS:

TITLE-TERMS: POWER SYSTEM MIMIC DEVICE SIMULATE COMPUTER CALCULATE
POWER SYSTEM
CHARACTERISTIC WAVE FORM GENERATOR CIRCUIT NOABSTRACT

DERWENT-CLASS: X12

EPI-CODES: X12-H;

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [Gmail](#) [more ▾](#)

[Sign in](#)

Google

"chip designing" + "power modeling"

Search

[Advanced Search](#)
[Preferences](#)

Web

Results 1 - 2 of 2 for "**chip designing**" + "**power modeling**". (0.12 seconds)

Tip: Try removing quotes from your search to get more results.

[PDF] [Communication Architecture Synthesis of Cascaded Bus Matrix](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Probably possible to extend to network-on-**chip designing**. Need much more improvements ... Need more accurate performance/area/**power modeling**. methods.
www.aspdac.com/aspdac2007/pdf/archive/2B-3.pdf - Similar pages

[PDF] [JAHRESBERICHT ANNUAL REPORT](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

chip. Designing suitably improved embedded systems requires a very **power modeling** and estimation in digital circuits. Creating a central market place ...
www.offis.de/presse/jahresbericht/jb2002/JB2002.pdf - Similar pages

Try [Google Desktop](#): search your computer as easily as you search the web.

"chip designing" + "power modeling"

Search

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied?](#) [Help us improve](#)

©2007 Google - [Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [Gmail](#) [more ▾](#)

[Sign in](#)

[Google](#)

("chip designing" or "semiconductor design" or

[Search](#)

[Advanced Search](#)
[Preferences](#)

Try uppercase "**OR**" to search for either of two terms. [\[details\]](#)

Web

Tip: Try removing quotes from your search to get more results.

Your search - ("**chip designing**" or "**semiconductor design**" or "**wafer design**") + "**power modeling**" - did not match any documents.

Suggestions:

- Make sure all words are spelled correctly.
- Try different keywords.
- Try more general keywords.
- Try fewer keywords.

©2007 Google - [Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [Gmail](#) [more ▾](#)

[Sign in](#)

[Google](#)

"ASIC power" + "simulation" + "derivative"

[Search](#)

[Advanced Search](#)
[Preferences](#)

Web

Results 1 - 10 of about 16 for "ASIC power" + "simulation" + "derivative". (0.25 seconds)

[PDF] High Speed Design Techniques in SoC – From Architecture to layout

File Format: PDF/Adobe Acrobat - [View as HTML](#)

derivative products requiring shorter introduction times. related to the SoC **ASIC's**

power consumption (e.g. lower reference clock frequency ...

www.ssiipex.com/cn/documents/learningcenter/HighSpeedDesignTechniquesi0SoC.pdf -

[Similar pages](#)

program 1996

A new **simulation** based design verification system, based on design error and then get into the lead and **derivative** products of the second generation. ...

asic.union.edu/Asic96/program.html - 53k - [Cached](#) - [Similar pages](#)

[PDF] A MICROMECHANICAL GYRO PACKAGE WITH GPS UNDER DEVELOPMENT FOR ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

space applications is a more accurate **derivative** from. this MMIMU development activity. ...

CMOS ASIC, power conditioning and trim circuitry, ...

microsat.sm.bmstu.ru/e-library/Algorithms/Hardware/Sensors/drapeer.pdf - [Similar pages](#)

Chapter 1 LIBRARIES

Power and ground pads provide connections to the various **ASIC power** and In order to minimize the error value, the first **derivative** of Equation 1.4.7 ...

www.springerlink.com/index/I035nx7u2044h146.pdf - [Similar pages](#)

The cydra 5 minisupercomputer: Architecture and implementation

for running Cydrix (an AT&T System V **derivative**) and programming environment tasks

ASIC power. dissipation ranged from 7 Watts to 12 Watts each. ...

www.springerlink.com/index/Q530280N38705528.pdf - [Similar pages](#)

[PDF] J-LINK Frequently Asked Questions (FAQ) General Questions

File Format: PDF/Adobe Acrobat - [View as HTML](#)

the J-LINK interface circuit runs off a **derivative** of the processor clock it ... circuit in a

simulation environment. If this is your first J-LINK design, ...

homepage.ntlworld.com/david.adshead/J-LINK_FAQ.pdf - [Similar pages](#)

[PDF] System Design Frontier

File Format: PDF/Adobe Acrobat

derivative products are created by adding or replacing the blocks that related to the SoC

ASIC's power consumption (e.g. lower reference clock ...

www.hwswworld.com/pdfs/frontier4.pdf - [Similar pages](#)

IEEE Transactions on Power Delivery IEEE Trans. Power Delivery ...

Networks-part III, voltage sag studies **simulation** Modeling power integrated circuit

(ASIC) power distribution power system communications power ...

ieeexplore.ieee.org/iel5/61/61.xml - [Similar pages](#)

High-efficiency, high-density MHz magnetic components for low ...

The core is a **derivative** of. pot core, it has sufficient electromagnetic shielding as well as area of custom **ASIC power** supply control chips, ...

ieeexplore.ieee.org/iel1/28/8965/00395298.pdf - [Similar pages](#)

Power Modeling of Embedded Memories

Table 1.1: Short and long term forecast of **ASIC power** consumption [65,66].

simulation models and possibly EDA tool specific abstracts (Synopsys DB ...

deposit.ddb.de/cgi-bin/dokserv?idn=967514649&

dok_var=d1&dok_ext=pdf&filename=967514649.pdf - [Similar pages](#)

[1](#) [2](#) [Next](#)

Download [Google Pack](#): free essential software for your PC

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

©2007 Google - [Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)



[Web](#) | [Images](#) | [Wikipedia](#) | [Video](#) | [More »](#)

[Sign in](#) | [Preferences](#)

"semiconductor power" + "power simulation"

Web Search [Adv](#)

☐ All the web ☒ Pages written in English

No document matched your search

"semiconductor power" + "power simulation"

Suggestions :

- *Check the spelling of your terms*
- *Try different or more general terms*
- *Try to remove some terms*
- *Check the syntax of your request*
- *Frequent terms have to be quoted to be searched for*



Dissatisfied with results? [Help us improve.](#)

[Our Blog](#) | [Create an account](#) | [Jobs Opportunities](#) | [Enterprise solutions](#) | [Site Map](#) | [Go to Exalead.com](#)

Find what you're looking for with Exalead, the innovative worldwide Web search engine.

Searching 8,042,419,228 web pages - © 2000-2007 exalead

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [Gmail](#) [more](#) ▾

[Sign in](#)

Google

"power modeling simulation"

Search

[Advanced Search](#)
[Preferences](#)

Web

Results 1 - 10 of about 138 for "**power modeling simulation**". (0.38 seconds)

Fluid Power Modeling, Simulation Software - Brief Article Diesel ...

Fluid **Power Modeling, Simulation** Software - Brief Article from Diesel Progress North American Edition in Array provided free by LookSmart Find Articles.

findarticles.com/p/articles/mi_m0FZX/is_2_66/ai_60471724 - 23k - Cached - Similar pages

Find Articles in Diesel Progress North American Edition: Feb 2000

Bibliography for "Fluid **Power Modeling, Simulation** Software - Brief Article". "Fluid **Power Modeling, Simulation** Software - Brief Article". ...

findarticles.com/p/articles/mi_m0FZX/is_2_66?pnun=2&opg=60471724 - 23k -

Cached - Similar pages

[[More results from findarticles.com](#)]

Power modeling methodology for a pipelined processor - US Patent ...

2 shows a prior art full-chip **power modeling simulation** in a microprocessor. CPU activity data is generated each cycle (20) and combined with power values ...

www.patentstorm.us/patents/6895561-description.html - 25k - Cached - Similar pages

Data analysis techniques for dynamic power simulation of a CPU ...

A method of data analysis for a **power modeling simulation**, [0008] In one aspect, a method for analyzing a **power modeling simulation** comprises receiving ...

www.freepatentsonline.com/20030110019.html - 32k - Cached - Similar pages

ARTIST2 NoE - Design for Low Power

... power modes and de-synchronizing in the context of the OpenCores AVR core (2) **Power modeling: Simulation**-based power modeling and estimation techniques ...

www.artist-embedded.org/artist/Design-for-Low-Power.html - 45k - Cached - Similar pages

Modeling of large wind farms in power system simulations - Power ...

wind **power, modeling, simulation**, wind farm, grid, interaction, power system dynamics, wind turbine aggregation. I. INTRODUCTION ...

ieeexplore.ieee.org/iel5/8076/22348/01043286.pdf - Similar pages

PowerFITS: Reduce Dynamic and Static I-Cache Power Using ...

panalyzer" [26] to run **power modeling simulation** for our experiments. "sim-panalyzer" is an infrastructure for microarchitectural power simulation at the ...

ieeexplore.ieee.org/iel5/9783/30850/01430557.pdf?arnumber=1430557 - Similar pages

[[More results from ieeexplore.ieee.org](#)]

An Energy Efficient Instruction Set Synthesis Framework for Low ...

In this paper, we used a modified version of the "sim-panalyzer" [27] to run the **power modeling simulation** for our experiments. ...

doi.ieeecomputersociety.org/10.1109/TC.2005.89 - Similar pages

A better way to model fluid-power systems | Technology > Computer ...

Realize greater benefits from CFD · Fluid **Power Modeling, Simulation** Software. Using Windows XP Automatic Update. T T T. art_print_gray Print art_email ...

www.allbusiness.com/technology/computer-software/1136871-1.html - 52k -

Cached - Similar pages

Keith A. Carangelo - Resume

Wrote **power modeling simulation** tools in C++ under HP-UX. Lead a six-person TQC process improvement team that cut power measurement time in half. ...

www.kcaran.com/resume/ - 18k - [Cached](#) - [Similar pages](#)

[1](#) [2](#) [3](#) **[Next](#)**

Try [Google Desktop](#): search your computer as easily as you search the web.

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

©2007 Google - [Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)



power modeling simulation

search

advanced search
preferences

Install the jux

Best Results

Compare Google's
ResultsCompare Yahoo's
ResultsCompare I
Result

Best results from all search engines:

Sponsored

Power Modeling Simulation

Shop for Power Modeling Simulation, and deals on tons of other products at MonsterMarketplace.
<http://www.monstermarketplace.com>

Power Modeling Simulation in the Free Online Encyclopedia

Read about Power Modeling Simulation in the free online encyclopedia and dictionary. Over 700,000 articles on any topic.
<http://www.thefreedictionary.com>

RF Power Simulation & Modeling Services

DESIGN SIMULATION MODELING MEASUREMENT RF Power Simulation & Modeling Services Until very recently, RF transistor, circuit, and amplifier designs have had to proceed largely in the ...
http://www.dbmengineering.com/images/dbmengineering_simulation.pdf [**#1 on MSN**]

Early DesignPhase Power/Performance Modeling through Statistical ...

Early DesignPhase Power/Performance Modeling through Statistical Simulation Lieven Eeckhout Koen De Bosschere Department of Electronics and Information Systems (ELIS), Ghent ...
<http://trappist.elis.ugent.be/~leeckhou/papers/ispass2001.pdf> [**#2 on MSN**]

Andrew Keefe Consulting: Power System Modeling, Simulation, and ...

Power System Types. Motors and Generators; DC-DC **Power** Converters and DC-AC Inverters; Fuel Cell Systems; Photovoltaic Systems; Feed-forward and feedback control systems
<http://www.andrewkeefeconsulting.com/> [**#3 on MSN**]

Power Electronics and Electrical Drives Modeling and Simulation ...

CASPOC is a powerful, innovative system simulator designed for the **simulation** of **Power Electronics** and **Electrical Drives**. Using **CASPOC**, **modeling power** electronics, electrical ...
<http://www.integratedsoft.com/brochures/Caspoc.pdf> [**#4 on MSN**]

System Level Power Modeling and Simulation of High-End Industrial ...

Today's System on Chip (SoC) technology can achieve unprecedented computing speed that is shifting the design bottleneck from computation capacity to communication bandwidth ...
<http://csdl.computer.org/comp/proceedings/date/2004/2085/03/208530318abs.htm> [**#5 on MSN**]

Power Simulation

In conjunction with StateGen, StimGen and LibChar, it is a complete package which contains all the required components for fast, accurate and reliable **power simulation** and **modeling**.
<http://www.libtech.com/node35.html> [**#6 on MSN**]

Virtual Power Plant Modeling and Simulation

04/2007 Advanced Process Engineering Co - **simulation** Description The National Energy Technology Laboratory (NETL) and its R&D collaboration partners are developing the ...
<http://www.netl.doe.gov/publications/factsheets/project/Proj347.pdf> [**#7 on MSN**]

AEgis Modeling and Simulation Products Overview

... **modeling** and **simulation**, electronics **simulation**, missile **modeling** and **simulation**, petrochemical, **pow**

generation, and process control **modeling and simulation** ...

http://www.aegistg.com/Modeling_Simulation_Products.htm [**#8 on MSN**]

[jux2 Blog](#) - [About jux2](#) - [Search Engine Stats](#) - [Privacy Policy](#) - [Feedback](#)

© copyright 2007 jux2™

All brand and product names are trademarks of their respective holders.



A Better Way to Search

"power modeling" + "semiconductor" + "derivative"

search

[advanced search](#)
[preferences](#)

[Install the jux](#)

Best Results

[Compare Google's Results](#)

[Compare Yahoo's Results](#)

[Compare I Result](#)

Best results from all search engines:

Sponsored

Power Modeling Semiconductor Derivative

Shop for Power Modeling Semiconductor Derivative, and deals on tons of other products at MonsterMarketplace.

<http://www.monstermarketplace.com>

Power Modeling Semiconductor Derivative in the Free Online Encyclopedia

Read about Power Modeling Semiconductor Derivative in the free online encyclopedia and dictionary. Over 700,000 articles on any topic.

<http://www.thefreedictionary.com>

An interconnect energy model considering coupling effects - Computer ...

For interconnect power modeling, the model has been commonly used. ... is the time derivative of. is the cur of edge, and we used (8) ...

cadlab.cs.ucla.edu/~cong/papers/tcad02_power.pdf [**#1 on Yahoo!**]

The Technology Collaborative : Round 10 (Spring 2004)

... on High-Density Tritium Locking Technology in Semiconductor ... of MPC over traditional proportional-integ derivative ... in embedded control systems. " Transaction Level Power Modeling ...

http://www.techcollaborative.org/default.aspx?id=round_10 [**#1 on MSN**]

Nanometer Power Leakage

As semiconductor structures become smaller, device and interconnect ... and is dependent on the power mo of intellectual property (IP), which ...

www.tayden.com/publications/NanometerPowerLeakage.pdf [**#2 on Yahoo!**]

TechOnline | A Great DATE in the City of Lights for Chip Designers

... low-voltage design initiative, along with SIP power modeling. ... on designs specified in Handel-C, an ANSI derivative ... Amphion Semiconductor: Ansoft: Applied Wave Research

<http://www.techonline.com/showArticle.jhtml?articleID=192200534> [**#2 on MSN**]

Low-Power Coalition

... ChipVision, Intel, LSI, Magma, NXP (formerly Philips Semiconductor) ... Power Modeling WG. Joint WG a JDM WG. Architecture WG. LPC TSG. Power Format WG ...

synopsys.com/news/events/.../presentations/PowerSession_Si2_LPC.pdf [**#3 on Yahoo!**]

UVA Computer Science: Technical Reports

... approach to find the relationship between the power modeling ... The most prominent feature of AspectJ ai derivative ... Gordon Moore observed that the capacity of semiconductor ...

<http://www.cs.virginia.edu/~techrep/index.html> [**#3 on MSN**]

EETimes.com - ALF facilitates SI-aware design flow

Particularly important is a power modeling capability utilizing the rich syntax ... Tundra Semiconductor seekin Senior Engineer, ASIC Design in South ...

www.eetimes.com/news/design/showArticle.jhtml?articleID=16505180 [**#4 on Yahoo!**]

Accurate Estimation of Global Buffer Delay Withina Floorplan

This work was supported in part by the Semiconductor Research ... Taking the derivative with respect to k , s the ... digital design, mixed analog-digital ICs, power modeling and ...
<http://dropzone.tamu.edu/~jhu/publications/AlpertTCAD06.pdf> [**#4 on MSN**]

Nanometer Prototyping

As the semiconductor industry moves into new dimensions nanometer areas, ... High-Level Power Modeling, Evaluation, and. Optimization. ...
[www.tayden.com/publications/Nanometer Prototyping.pdf](http://www.tayden.com/publications/Nanometer%20Prototyping.pdf) [**#5 on Yahoo!**]

ETD Collection for Oklahoma State University

Cheng Chih Liu (2007) Power modeling of CMOS digital circuits with a piecewise linear model ; 2007. Ada S: Yee Lo (2007) A study on the impact of a bundle of determinants on the ...
<http://e-archive.library.okstate.edu/dissertations> [**#5 on MSN**]

The Technology Collaborative : PDG Press Release: July 13th, 2004

... for hydrogen storage, semiconductor processing, and fiber optic ... The key deliverable will be a power modeling technique at the transaction level. ...
www.techcollaborative.org/default.aspx?id=press_jul_13_2004 [**#6 on Yahoo!**]

www.chipdesignmag.com

... CEO Mike Fister, who is looking forward to the challenges and vigorous sport of building Cadence to be an even bigger player in the EDA industry and the larger semiconductor supply ...
<http://www.chipdesignmag.com/edatation/august2004/> [**#6 on MSN**]

The Technology Collaborative : Round 10 (Spring 2004)

... Tritium Locking Technology in Semiconductor and Dielectric" ... "Transaction Level Power Modeling Methodology Using PCI- Express Core as the Design Driver" ...
www.techcollaborative.org/default.aspx?id=round_10 [**#7 on Yahoo!**]

ETDs Browse Page

Wen-Tsan Hsieh: On High-Level Power Modeling Approaches for IP-Based SOC Designs: Electrical ... to Configuring the Extended Supply Chain - An Implementation in the Semiconductor ...
http://thesis.lib.ncu.edu.tw/ETD-db/ETD-browse/browse?first_letter=H [**#7 on MSN**]

2004 SRC Annual Report

semiconductor ... HfSiON or a related derivative. These materials were first identified power modeling methodologies for reducing power dissipation in the ...
www.src.org/member/about/arch/annual04.pdf [**#8 on Yahoo!**]

Page Not Found

MonsterMarketplace Shopping Directory. Page not found. ... Pottery Barn® Bed High quality beds & headboard in classic designs for every room.
<http://www.monstermarketplace.com/search> [**#8 on MSN**]

An Optimal Analytical Solution for Processor Speed Control with Thermal ...

As semiconductor manufacturing technology scales to smaller device sizes, power aware performance and power modeling microarchitecture level. IEEE Trans. ...
www.public.asu.edu/~ashriva6/seminar/09-08-2006-Ravishankar3.pdf [**#9 on Yahoo!**]

UVA Computer Science: Technical Reports

... the relationship between the power modeling granularity and the associated peak ... prominent feature of AspectJ and derivative languages is the aspect. ...
www.cs.virginia.edu/~techrep [**#10 on Yahoo!**]



Customize your homepage

[Web](#) | [Images](#) | [Wikipedia](#) | [Video](#) | [More »](#)

[Sign in](#) | [Preferences](#)

"semiconductor power" + "derivative"

[Web Search](#)

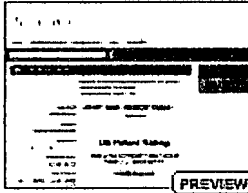
[Adv](#)

☐ All the web ☒ Pages written in English

[Web](#)

Results 1-3 of about 3 for "semiconductor power" + "derivative"

View:



Method for estimating the service life of a power semiconductor ...

5521524 Method and system for screening ... load changes, calculating a derivative dP/dN of the electrical parameter P according to the number ...

www.patentstorm.us/patents/5877419.html - 17k - Add to shortcuts

Narrow your search

Your refinements

Languages

• English [remove](#)

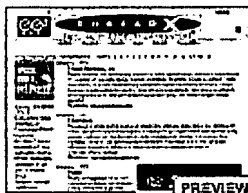
[More choices »](#)



Robust adaptive fuzzy control for permanent magnet synchronous ...

Takagi-Sugeno-type fuzzy logic systems are used to approximate uncertain functions. ... The closed-loop system is proven to be semiglobally uniformly ultimately ...

www.robo-erectus.org/members/czhou/Paper/YangZhouJIS05.pdf - 07 Sep 2006 - 217k - Add to shortcuts



companies (p) - tutorials free design (p)

IEI Technology: PICMG, PC/104, Embedded, RISC, PCI Express & more ... interfacing solutions for the PCI Express, PCI, PCI-X, and derivative protocols.

www.eg3.com/WebID/411/p/blank/company/10-p-p.htm - 25k - Add to shortcuts

Results page: 1

"semiconductor power" + "derivative"

[Web Search](#)

☐ All the web ☒ Pages written in English



Dissatisfied with results? [Help us improve.](#)

[Our Blog](#) | [Create an account](#) | [Jobs Opportunities](#) | [Enterprise solutions](#) | [Site Map](#) | [Go to Exalead.com](#)

Find what you're looking for with Exalead, the innovative worldwide Web search engine.

Searching 8,042,419,228 web pages - © 2000-2007 exalead

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [Gmail](#) [more](#) ▾

[Sign in](#)

Google

"Bogliolo" adaptive mean square

Search

[Advanced Search](#)
[Preferences](#)

[Web](#) [Books](#)

Results 1 - 10 of about 252 for "**Bogliolo**" **adaptive mean square**. (0.28 seconds)

Adaptive Least Mean Square Behavioral Power Modeling - Bogliolo ...

In this work we propose an effective solution to the main challenges of behavioral power modeling the generation of models for the power dissipation of ...
citeseer.ist.psu.edu/283448.html - 23k - [Cached](#) - [Similar pages](#)

Parameterized RTL Power Models for Soft Macros (ResearchIndex)

5 **Adaptive least mean square** behavioral power modeling - **Bogliolo**, Benini et al. - 1997
ACM 2 A new parameterizable power macro-model for datapath compone. ...
citeseer.ist.psu.edu/**bogliolo**01parameterized.html - 19k - [Cached](#) - [Similar pages](#)
[More results from citeseer.ist.psu.edu]

Adaptive least mean square behavioral power modeling

Adaptive least mean square behavioral power modeling ... Alessandro **Bogliolo** , Luca Benini, Node sampling: a robust RTL power modeling approach, ...
portal.acm.org/citation.cfm?id=787698 - [Similar pages](#)

EDTC'97: Adaptive least mean square behavioral power modeling

Adaptive. Least Mean Square Behavioral. Power Modeling. Alessandro **Bogliolo**. Luca Benini. Giovanni De Micheli. DEIS - University of Bologna. Bologna - I ...
doi.ieeecomputersociety.org/10.1109/EDTC.1997.582391 - [Similar pages](#)

Welcome to IEEE Xplore 2.0: Adaptive least mean square behavioral ...

Adaptive least mean square behavioral power modeling **Bogliolo**, A. Benini, L. De Micheli, G. DEIS, Bologna Univ.; This paper appears in: European Design and ...
ieeexplore.ieee.org/xpls/abs_all.jsp?arnumber=582391 - [Similar pages](#)

Robust RTL Power Macromodels - Very Large Scale Integration (VLSI ...

[7] A. **Bogliolo** et al., "**Adaptive least mean square** behavioral power. modeling," in Proc. European Design and Test Conf., 1997, pp. 404-410. ...
ieeexplore.ieee.org/iel5/92/15863/00736131.pdf - [Similar pages](#)
[More results from ieeexplore.ieee.org]

Adaptive least mean square behavioral power modeling

Citation: A. **Bogliolo**, L. Benini, G. De Micheli, "**Adaptive least mean square** behavioral power modeling," edtc, p. 404, 1997 European Design and Test ...
csdl.computer.org/comp/proceedings/edtc/1997/7786/00/77860404abs.htm - [Similar pages](#)

Adaptive Least Mean Square Behavioral Power Modeling

Alessandro **Bogliolo**, Luca Benini, Giovanni De Micheli **Adaptive Least Mean Square Behavioral Power Modeling**. Title. **Adaptive Least Mean Square Behavioral Power ...**
nsdl.org/resource/2200/20061011214520105T - 12k - [Cached](#) - [Similar pages](#)

Alessandro Bogliolo - Publications

A. **Bogliolo**, L. Benini, and G. De Micheli, "**Adaptive least mean square** behavioral power modeling," in Proc. of IEEE European Design and Test Conference, pp. ...
www.sti.uniurb.it/**bogliolo**/publications.htm - 35k - [Cached](#) - [Similar pages](#)

Unified Low-Power Design Flow for Data-Dominated Multi-Media and ... - Google Books Result

by Jack Philip Holman, Francky Catthoor - 2000 - Technology - 208 pages

[4] A. **Bogliolo** et al., "**Adaptive Least Mean Square** Behavioral Power Modeling", Proc. ED&TC, 1997, pp. 404-10. [5] R. Burch et al., "A Monte Carlo Approach ...
books.google.com/books?isbn=0792379470...

1 2 3 4 5 6 7 8 9 10 **Next**

Download Google Pack: free essential software for your PC

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

©2007 Google - [Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

	Type	L #	Hits	DBs	Search Text	Time Stamp
1	BRS	L1	1	USPAT	"5877419".pn.	2007/10/23 12:30
2	BRS	L2	4217	USPAT	(derivative same power)	2007/10/23 12:30
3	BRS	L3	30	USPAT	(derivative same power) and (derivative same peak same average)	2007/10/23 12:30
4	BRS	L4	87	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(derivative same power) and (derivative same peak same average)	2007/10/23 12:30
5	BRS	L5	44	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(derivative same power) and (derivative same peak same average) and (circuits or wafers or semiconductors)	2007/10/23 12:31
6	BRS	L6	103	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(derivative same power) and (derivative same peak same average) and (circuits or wafers or semiconductors)	2007/10/23 12:33
7	BRS	L7	130397	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(circuits or wafers or semiconductors).ti. and (power).ti.	2007/10/23 12:33

	Type	L #	Hits	DBs	Search Text	Time Stamp
8	BRS	L8	129	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(circuits or wafers or semiconductors).ti. and (power).ti. and (simulation).ti.	2007/10/23 13:04
9	BRS	L9	555	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	"716"/\$.ccls. and derivatives	2007/10/23 13:04
10	BRS	L10	29	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	"716"/\$.ccls. and derivatives and (power same average)	2007/10/23 13:04
11	BRS	L11	9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	"716"/\$.ccls. and derivatives and (power same average) and mean	2007/10/23 13:12
12	BRS	L12	2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	"6895561".pn.	2007/10/23 13:18

	Comments	Error Definition	Errors
8			
9			
10			
11			
12			

	Type	L #	Hits	DBs	Search Text	Time Stamp
13	BRS	L13	18641	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(power same simulation)	2007/10/23 13:19
14	BRS	L14	33	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(power same simulation) and (spread adj sheet)	2007/10/23 13:20
15	BRS	L15	9	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(power same simulation) and (spread adj sheet) and (power same cycles)	2007/10/23 13:21
16	BRS	L16	2061	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(power same cycles) and CAD	2007/10/23 13:23
17	BRS	L17	184	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(power same cycles) and CAD and derivative	2007/10/23 13:25

	Comments	Error Definition	Errors
13			
14			
15			
16			
17			

	Type	L #	Hits	DBs	Search Text	Time Stamp
18	BRS	L18	1051	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(power adj plant) and average and mean and peak	2007/10/23 13:26
19	BRS	L19	37	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(power adj plant) and (average same mean same peak)	2007/10/23 13:27
20	BRS	L20	751	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	((power adj plant) same monitor\$3) and (monitor\$3 same power same levels)	2007/10/23 13:27
21	BRS	L21	31	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	((power adj plant) same monitor\$3) and (monitor\$3 same power same levels) and (power same average same peak)	2007/10/23 13:28
22	BRS	L22	4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	((power adj plant) same monitor\$3) and (monitor\$3 same power same levels) and (power same average same peak) and derivative	2007/10/23 13:30

	Comments	Error Definition	Errors
18			
19			
20			
21			
22			

	Type	L #	Hits	DBs	Search Text	Time Stamp
23	BRS	L23	279	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	((power adj plant) same monitor\$3) and simulation	2007/10/23 13:30
24	BRS	L24	117	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	((power adj plant) same monitor\$3) and (power same simulation)	2007/10/23 13:31
25	BRS	L25	83	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	((power adj plant) same monitor\$3) and (power same simulation) and (power same data)	2007/10/23 13:31
26	BRS	L26	29	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	((power adj plant) same monitor\$3) and (power same simulation) and (power same data) and (cycles same data)	2007/10/23 13:33
27	BRS	L27	15	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(multi-cycle same power) and (single-cycle same power)	2007/10/23 13:38

	Comments	Error Definition	Errors
23			
24			
25			
26			
27			

	Type	L #	Hits	DBs	Search Text	Time Stamp
28	BRS	L28	5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(multi-cycle same power) and (single-cycle same power) and simulation	2007/10/23 13:39
29	BRS	L29	5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(multi-cycle same power) and (single-cycle same power) and simulation and data	2007/10/23 13:39
30	BRS	L30	5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(multi-cycle same power) and (single-cycle same power) and simulation and data and (summary or sheet)	2007/10/23 13:40
31	BRS	L31	1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(multi-cycle same power) and (single-cycle same power) and simulation and data and (summary or sheet) and derivative	2007/10/23 13:48
32	BRS	L32	6	US-PGPUB; USPAT; USOCR	("5673321" "5815696" "5831885" "5907714" "6016543" "7124160").PN.	2007/10/23 13:44

	Comments	Error Definition	Errors
28			
29			
30			
31			
32			

	Type	L #	Hits	DBs	Search Text	Time Stamp
33	BRS	L33	1	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TDB	(multi-cycle same power) and (single-cycle same power) and simulation and data and (summary or sheet) and derivative and (difference or subtraction)	2007/10/23 13:49

Web Images Video News Maps Gmail more ▾

[Sign in](#)

Google

simulating power op-amps

Search

[Advanced Search](#)
[Preferences](#)

Web

Results 1 - 10 of about **165,000** for **simulating power op-amps**. (0.08 seconds)

Low-Power Op Amps Include Rail-to-Rail Inputs

Low-Power Op Amps Include Rail-to-Rail Inputs. Jul 11, 2007 1:08 PM ... Thermal Management **Simulation**. Power Systems DC-DC Converters ...
powerelectronics.com/power_management/news/intersil-pinpoint-precision-amps-rail-inputs-0711/ - 41k - [Cached](#) - [Similar pages](#)

Microchip Technology Low-Power Op Amps Maintain High Performance

Low-Power Op Amps Maintain High Performance. Apr 25, 2007 10:54 AM ... Thermal Management **Simulation**. Power Systems DC-DC Converters ...
powerelectronics.com/power_management/news/microchip-technology-mcp6031-op-amps-0425/ - 41k - [Cached](#) - [Similar pages](#)
[[More results from powerelectronics.com](#)]

STMicroelectronics - Amplifiers & Linear - Amplifiers & Linear ...

Operational Amplifier **Simulation** Models ... Input/Output Rail-to-Rail Low Power Op-Amps. TS971-TS972-TS974. Output Rail-to-Rail Very Low Noise Op-Amps ...
www.st.com/stonline/products/families/amplifiers_linear/opamps/opamp_macromodels.htm - 29k - [Cached](#) - [Similar pages](#)

Intusoft Newsletter, June 1999, ICAP/4, SPICE, Modeling Op-Amps ...

Op-amps are modeled using subcircuits (collections of elements) which can better ...
Therefore, **simulation** of many power supply topologies is impossible. ...
www.intusoft.com/nlhtml/nl56.htm - 29k - [Cached](#) - [Similar pages](#)

Spice links

Analog and RF Models, **Simulation** and modeling services for RF and microwave devices and circuits. Apex Microtechnology, **Power Op-amps** ...
www.intusoft.com/slinks.htm - 17k - [Cached](#) - [Similar pages](#)
[[More results from www.intusoft.com](#)]

Spice Circuit Simulation

SPICE is an acronym for **Simulation** Program with Integrated Circuit Emphasis and was
Power Op-amps. Burr-Brown. **Opamps** and other linear devices. ...
homepages.which.net/~paul.hills/Circuits/Spice/Spice.html - 1k - [Cached](#) - [Similar pages](#)

Free Spice Simulation App Supports 800 ADI Op Amps

Free Spice **Simulation** App Supports 800 ADI Op Amps September 19, 2007 ... MX multimedia processors and **Power** Management ICs deliver on performance while ...
eepr.com/Locator/Products/ArticleID/35481/35481.html - 39k - [Cached](#) - [Similar pages](#)

A consistent dynamic MOSFET model for low-voltage applications ...

is especially suited for the **simulation** of low-voltage mixed analog ... feasibility of ultra-low-power **OPAMPs** working at supply voltages well below 1V. Fig. ...
ieeexplore.ieee.org/iel5/6961/18736/00865327.pdf - [Similar pages](#)

Linear Technology - LT6203 - Single/Dual/Quad 100MHz, Rail-to-Rail ...

LT6203 - Single/Dual/Quad 100MHz, Rail-to-Rail Input and Output, Ultralow 1.9nVrHz Noise, Low **Power Op Amps**. Features; Description; Packaging; Applications ...
www.linear.com/pc/productDetail.jsp?navId=H0,C1,C1154,C1009,C1026,P2213 - 24k -